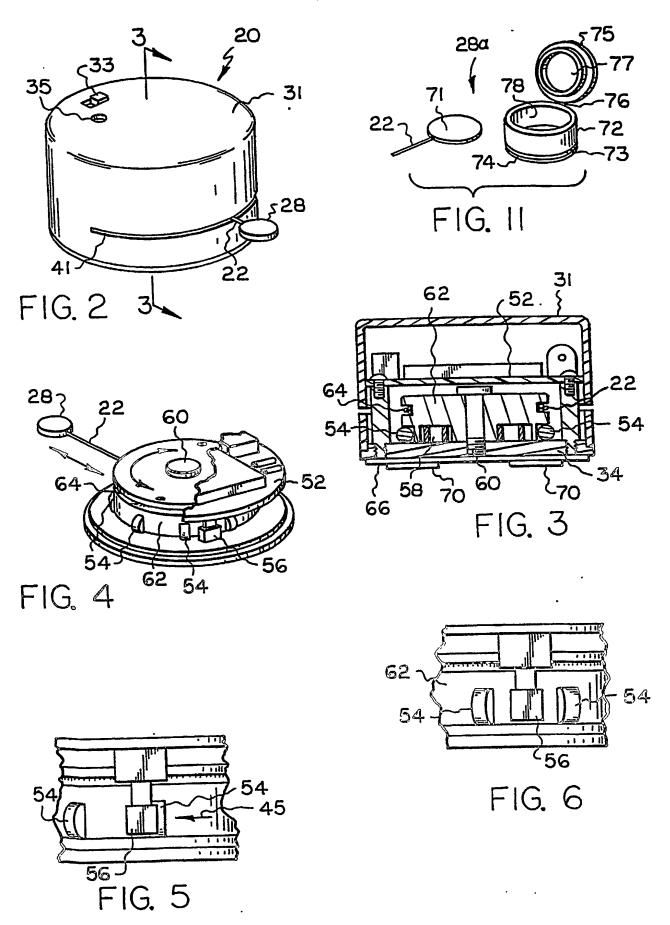
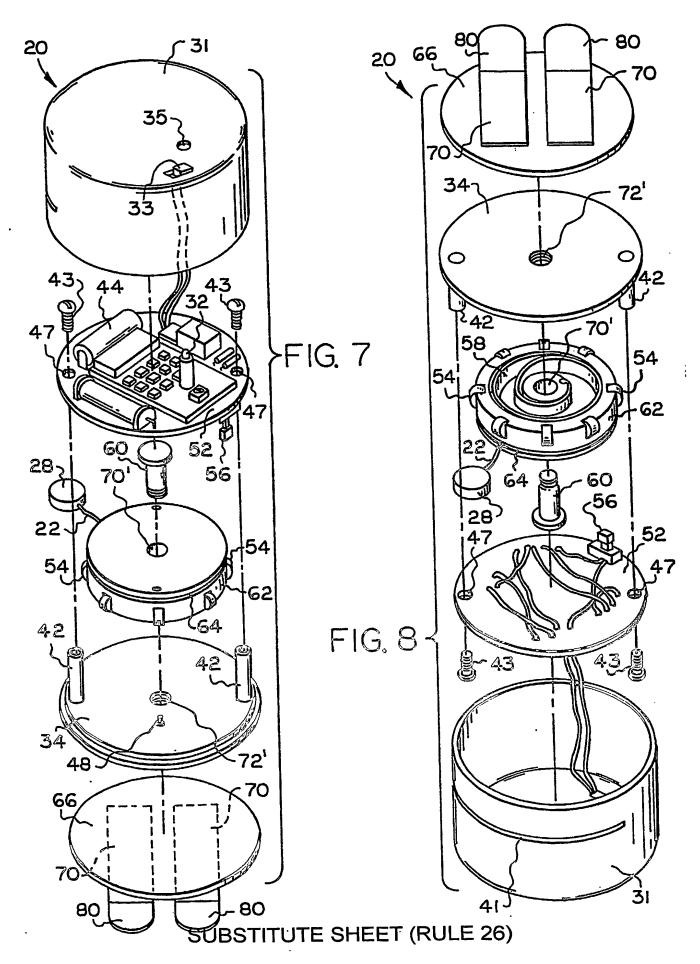
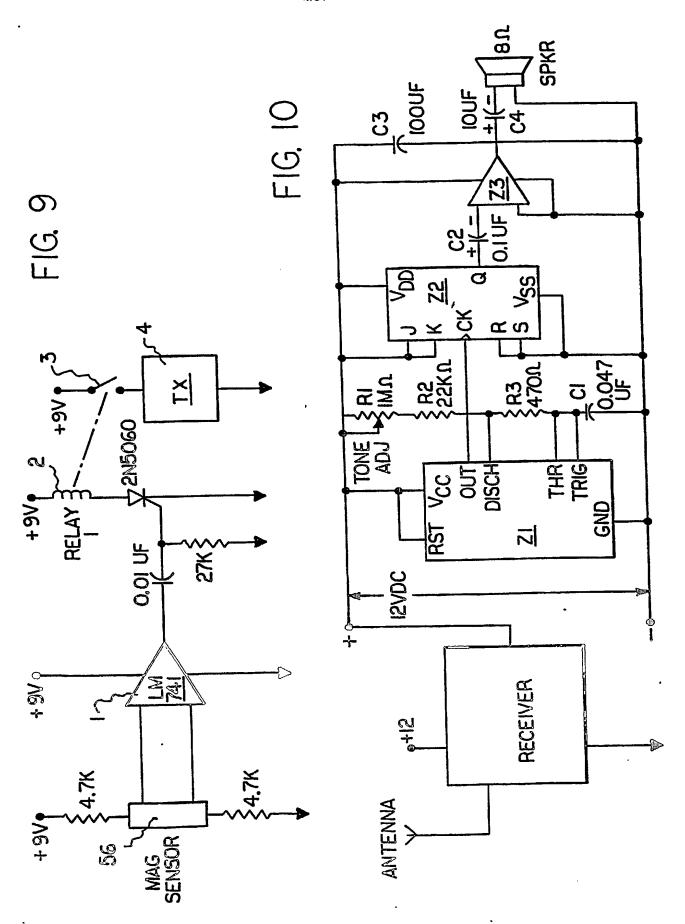


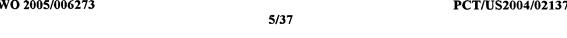
SUBSTITUTE SHEET (RULE 26)

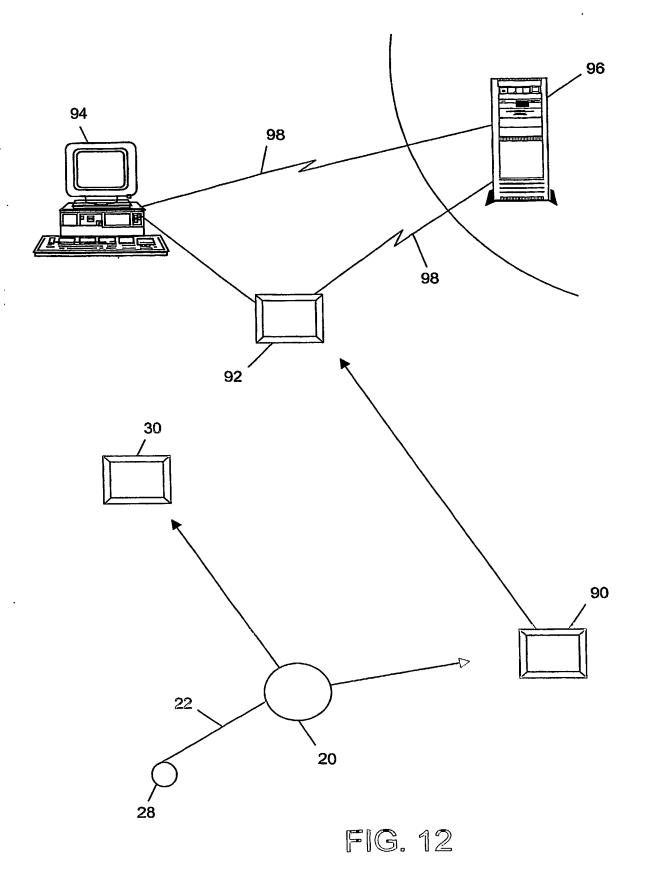




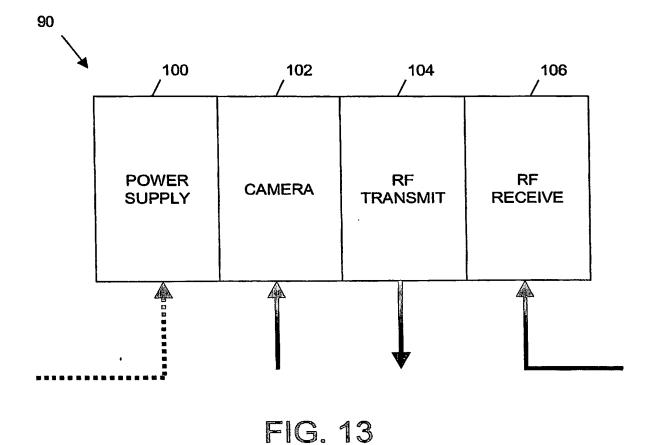


SUBSTITUTE SHEET (RULE 26)

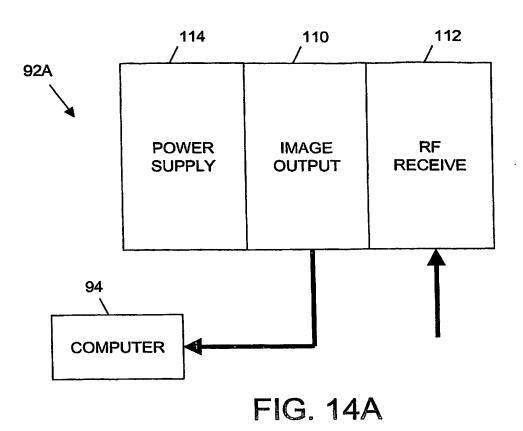




SUBSTITUTE SHEET (RULE 26)



SUBSTITUTE SHEET (RULE 26)



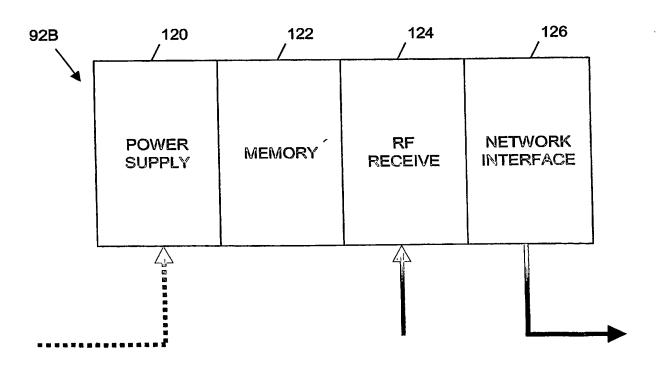


FIG. 14B

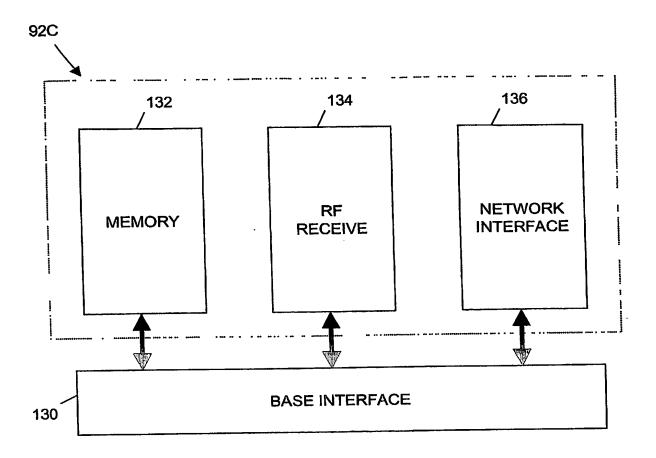
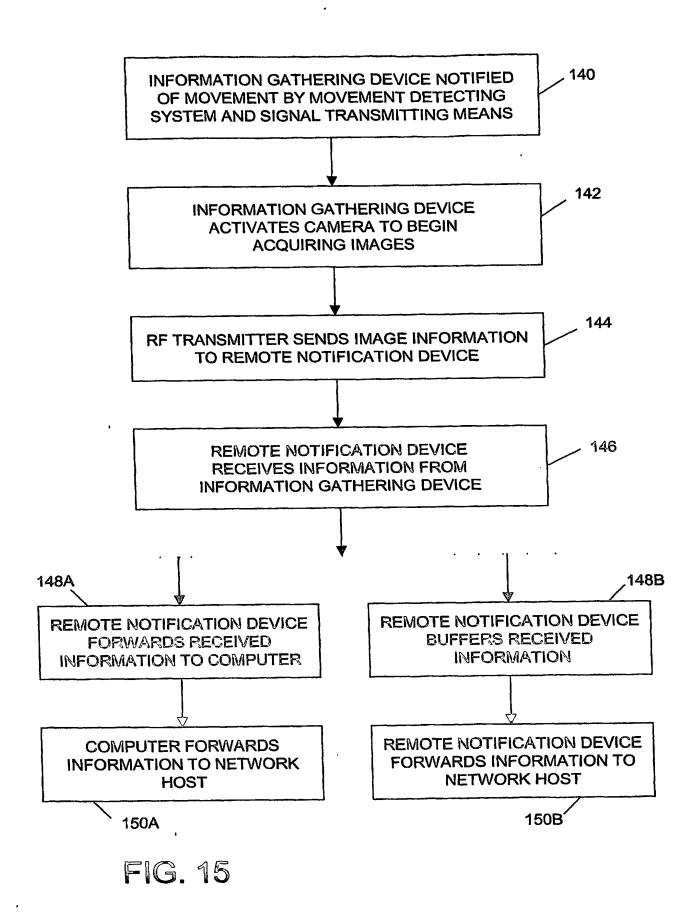
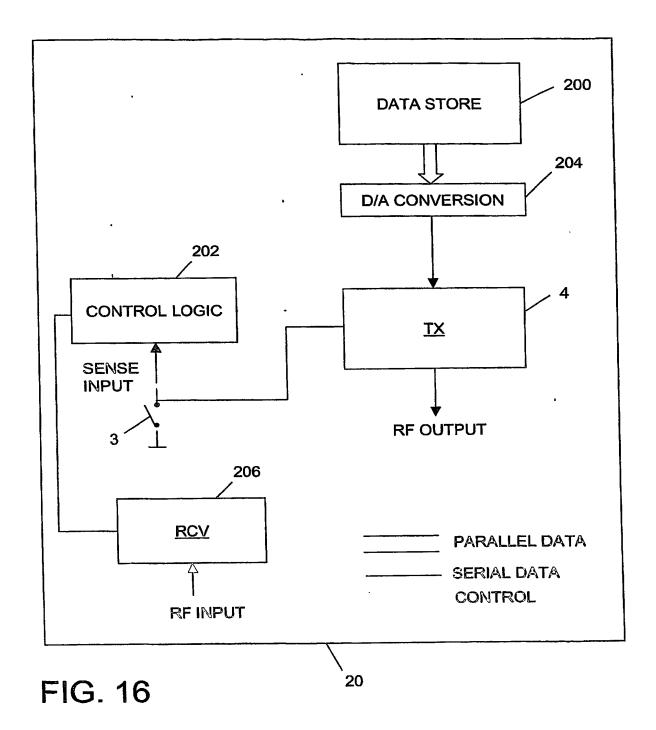


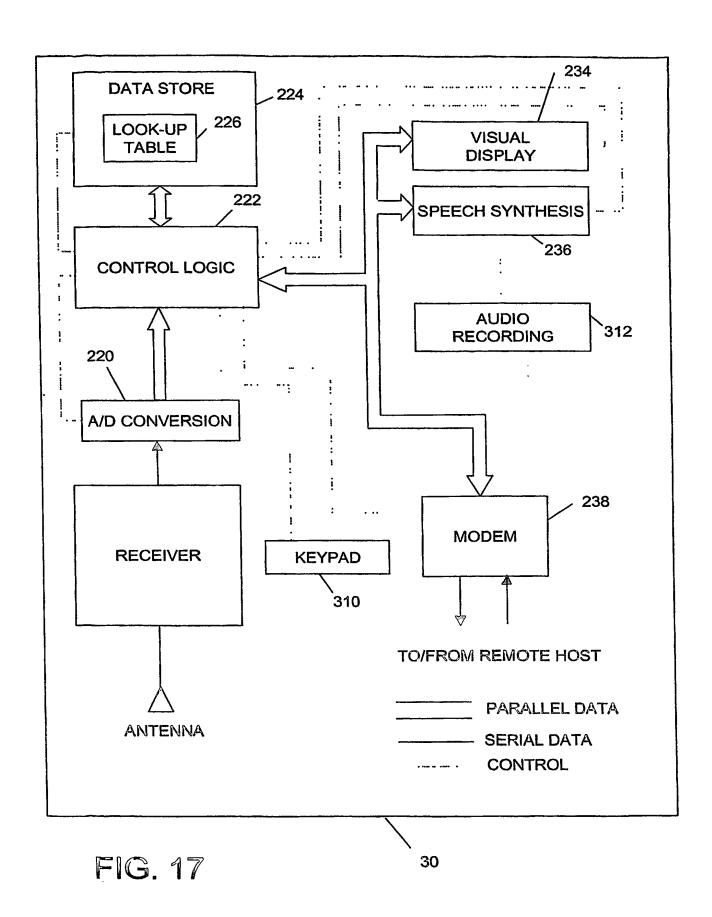
FIG. 14C



SUBSTITUTE SHEET (RULE 26)



SUBSTITUTE SHEET (RULE 26)



SUBSTITUTE SHEET (RULE 26)

	230	/232	
	xxyyzz00	FRONT DOOR	228
226	xxyyzz01	BACK DOOR	
*	·		
	:		
		1	
	:		
	:	·	
	٠		
	xxyyzznn	ANYWHERE	

FIG. 18

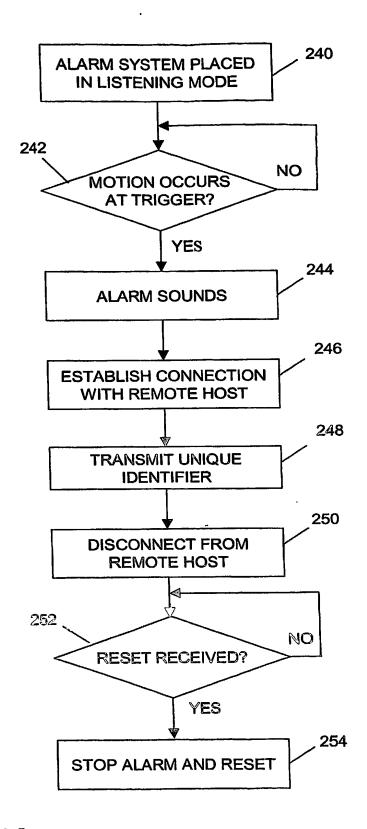


FIG. 19

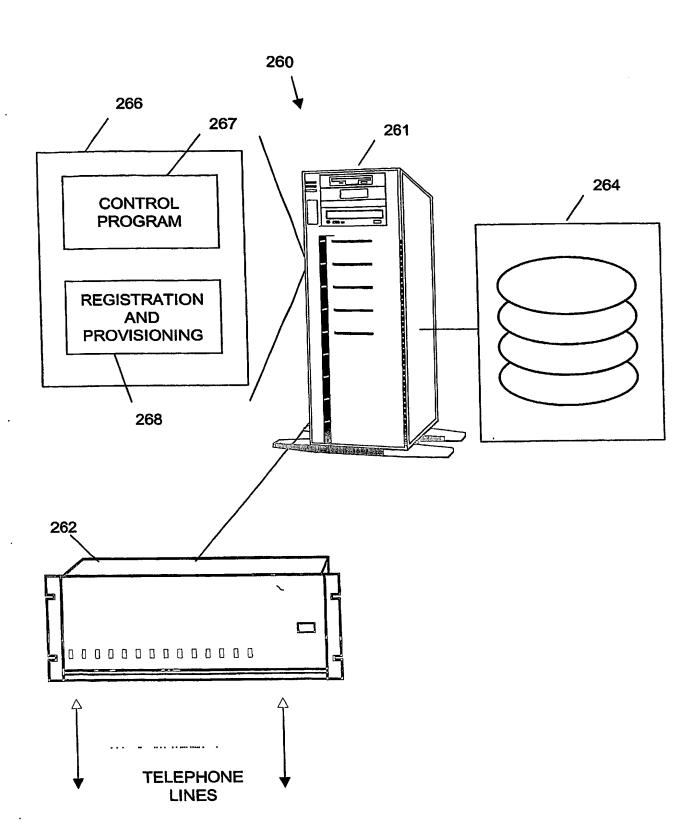
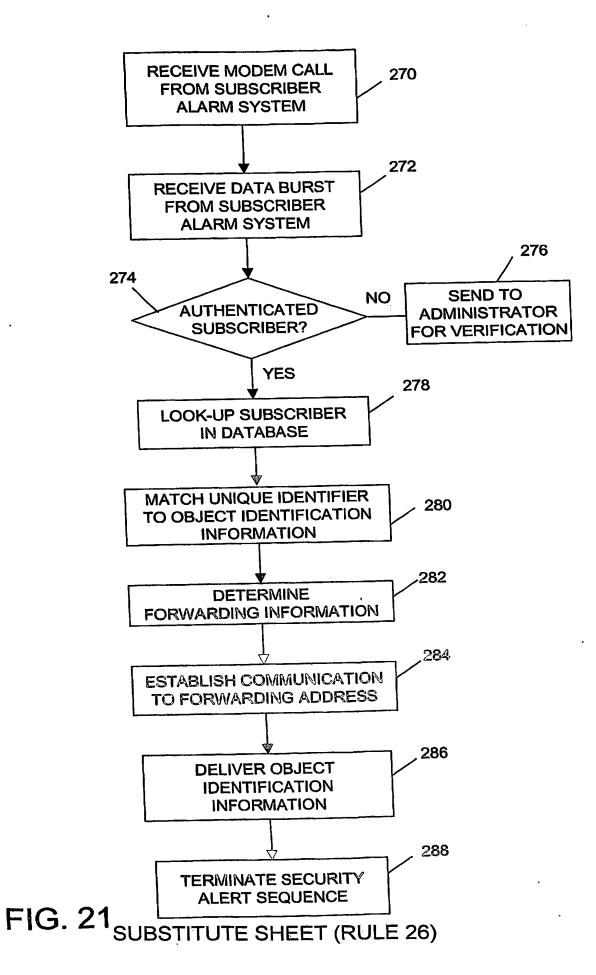
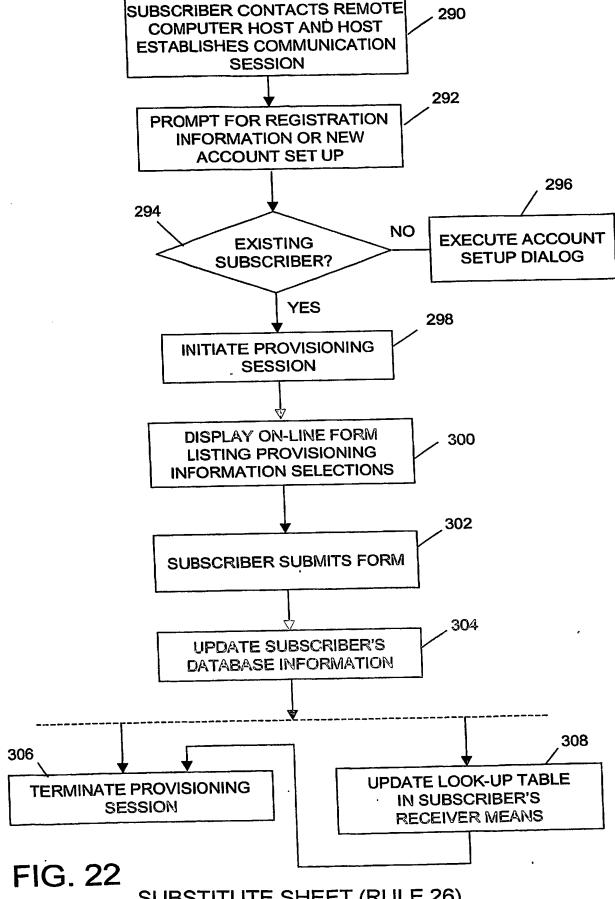


FIG. 20





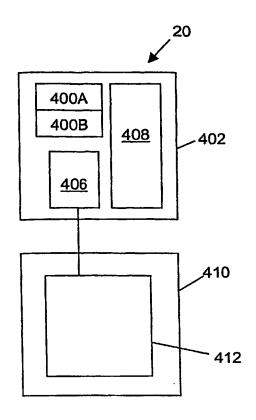
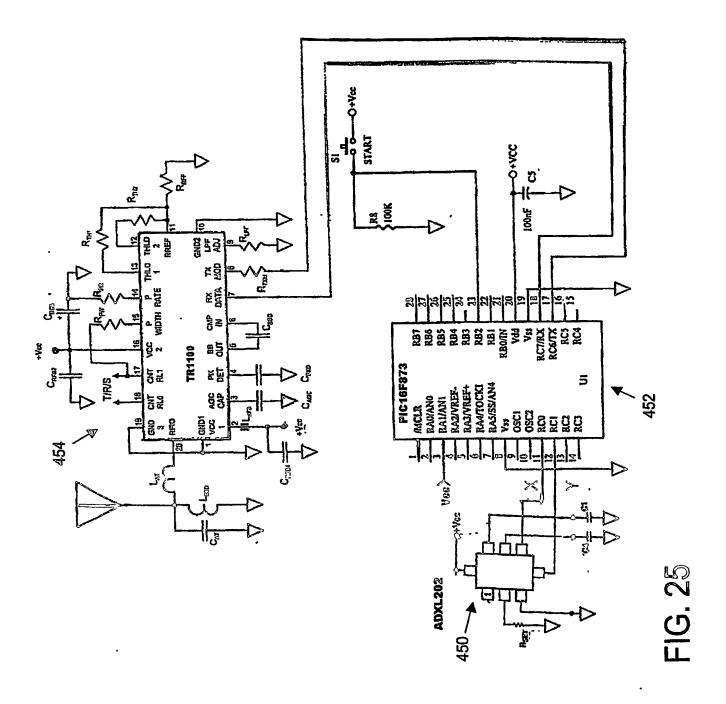


FIG. 23 20 VCC MIND 416 400A/B LOW CURRENT VOLTAGE DOUBLER WAKE WAKE (D) BATTERY BATTERY DETECTO ID_1 VCC (4.8V MIN) VCC CESA HIVE Q LOW VOLTAGE ENADLE 404 शखा 🗐 m_2 INFRARED MICRO-CONTROLLER OR RADIO FREQUENCY TRANSMITTER DTA_OUT e "aa" Datteries CSERIAL POINTER MICRUGYRU SVRET 2 VRET 2 406 ASIC (GYRATION ASIC #EU00057-001) ®∧e_5 %**c**_5 \$ GtiB en 🞯 VG_1 @vg_1 STO_CLI: DVREF_1 VREF_1 HSTR_CLK GND 418 414 FIG. 24

SUBSTITUTE SHEET (RULE 26)



SUBSTITUTE SHEET (RULE 26)

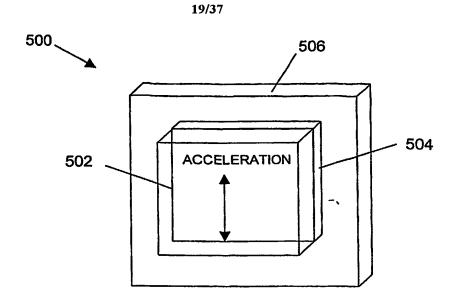


FIG. 26

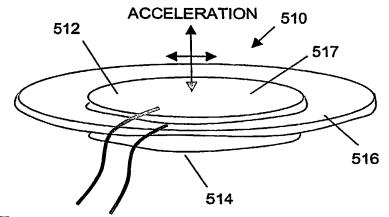


FIG. 27

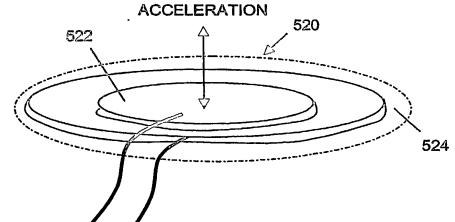
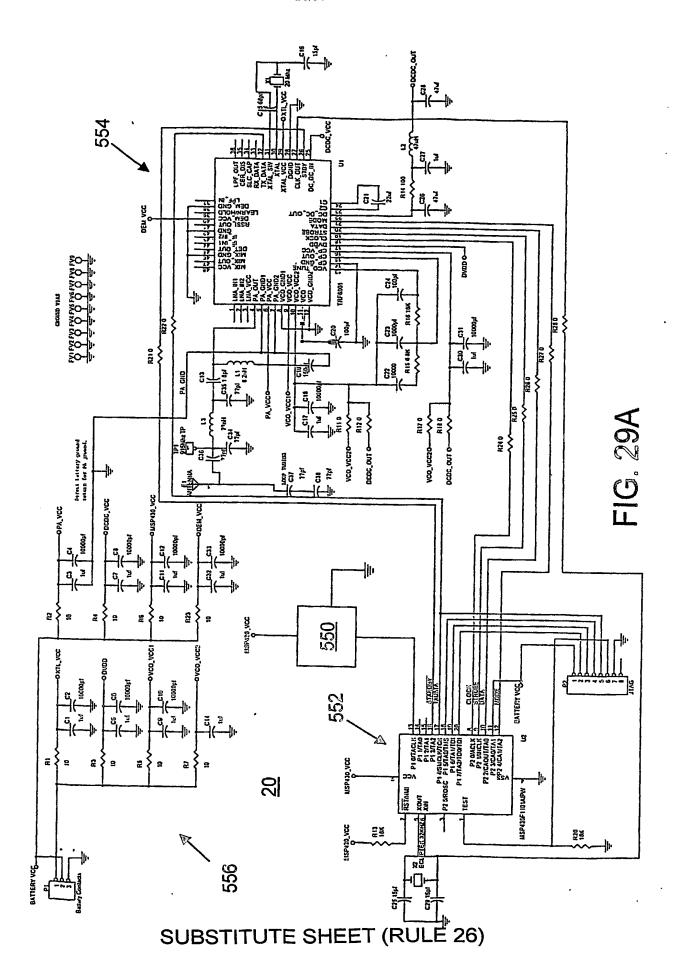
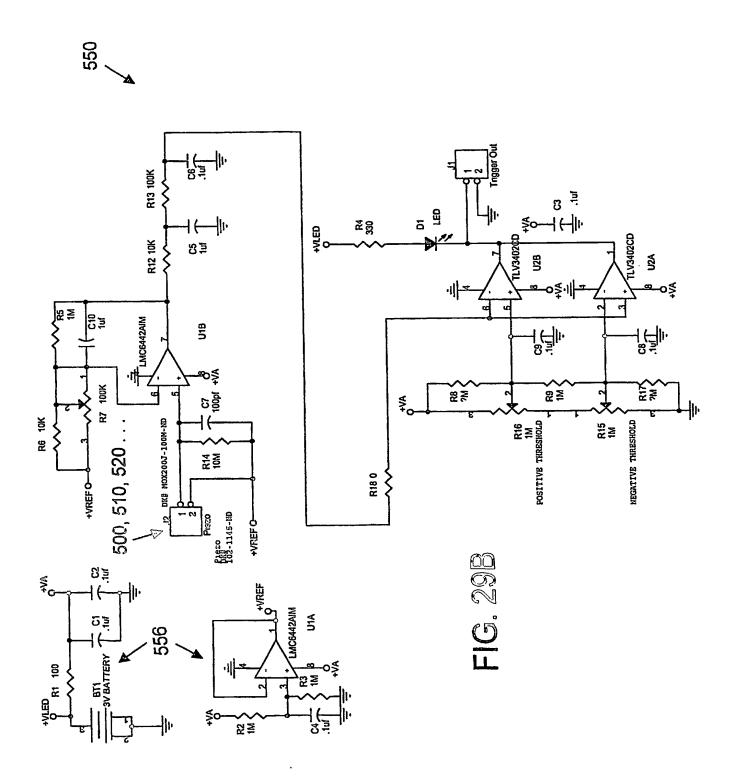


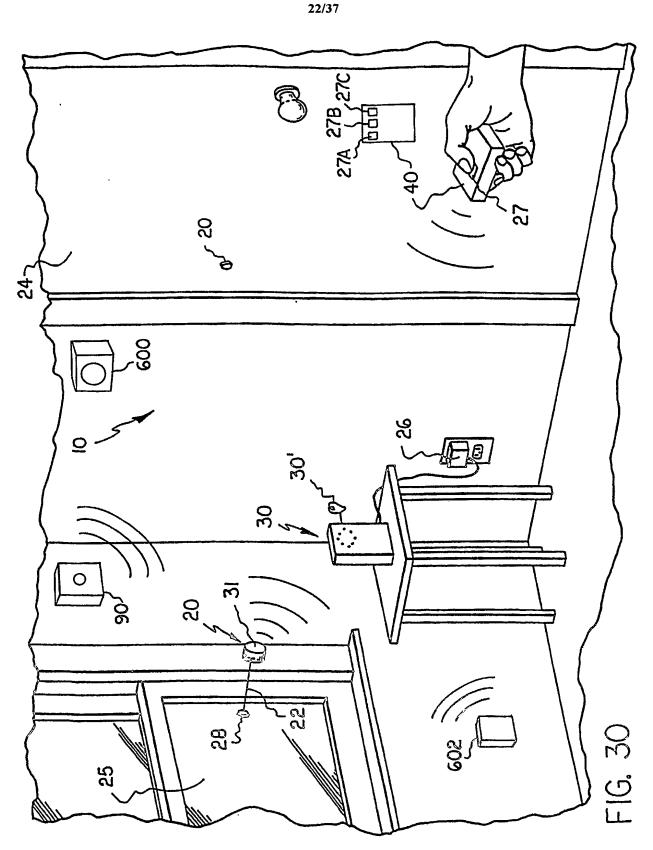
FIG. 28



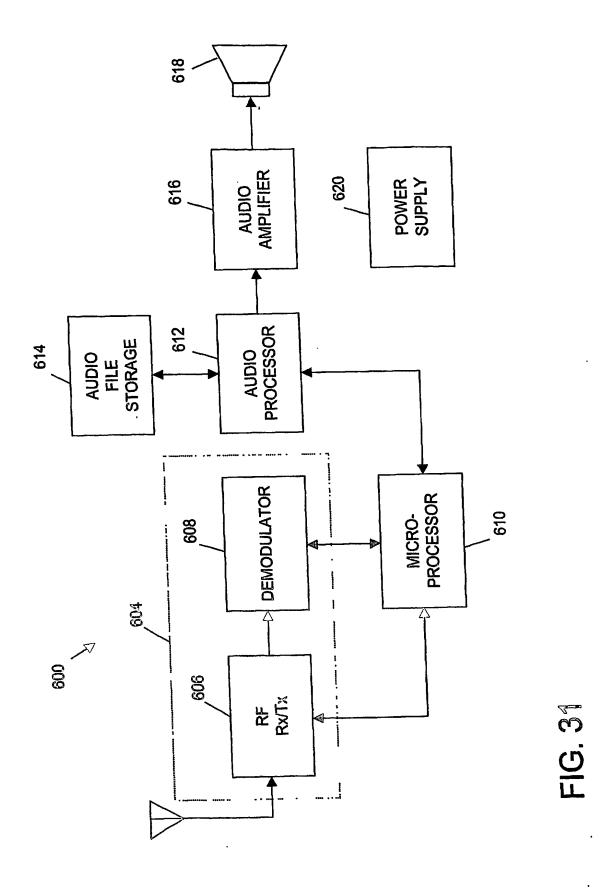


SUBSTITUTE SHEET (RULE 26)

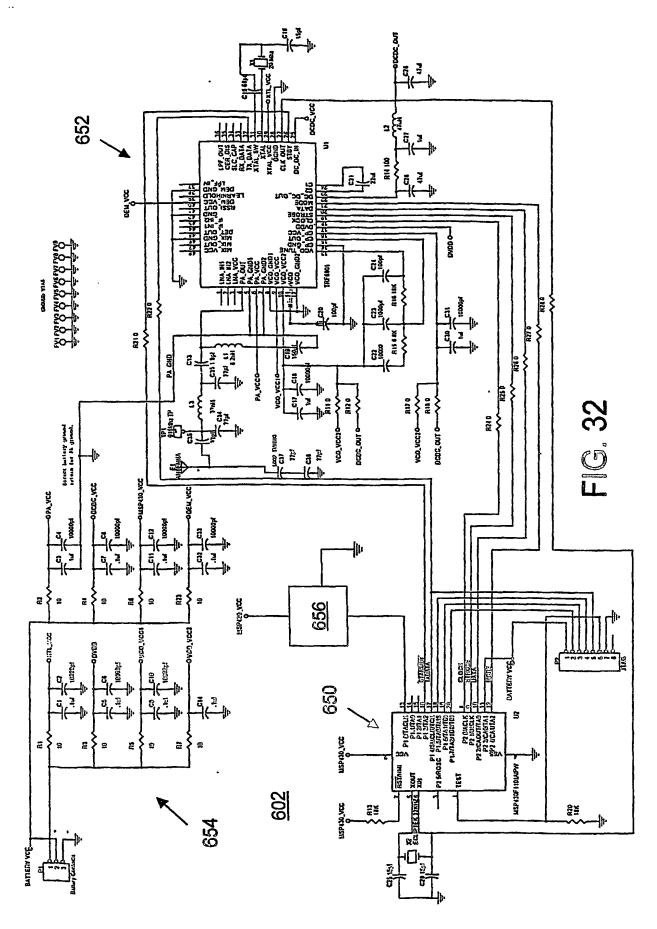




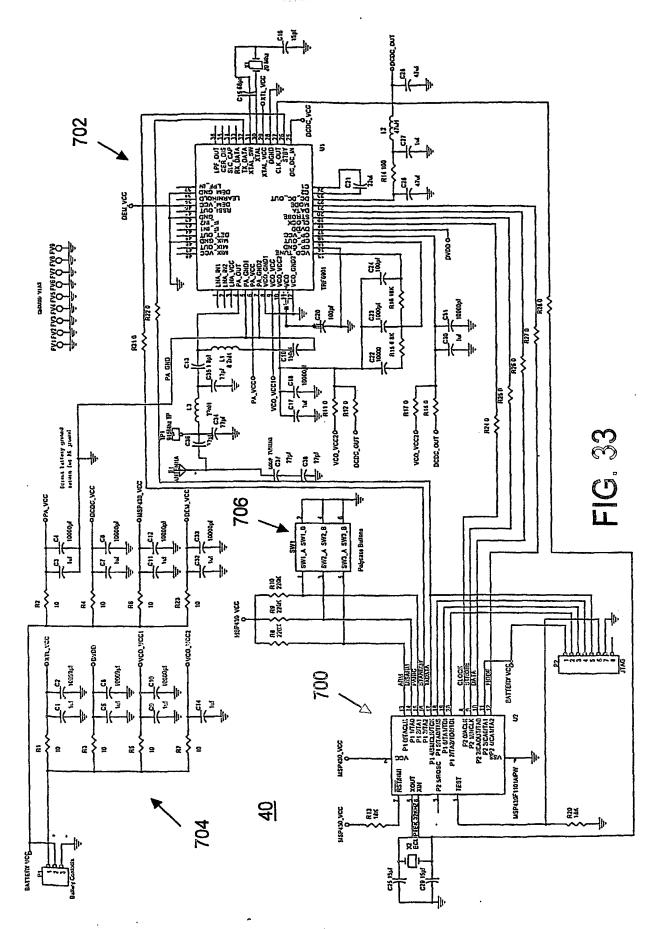
SUBSTITUTE SHEET (RULE 26)



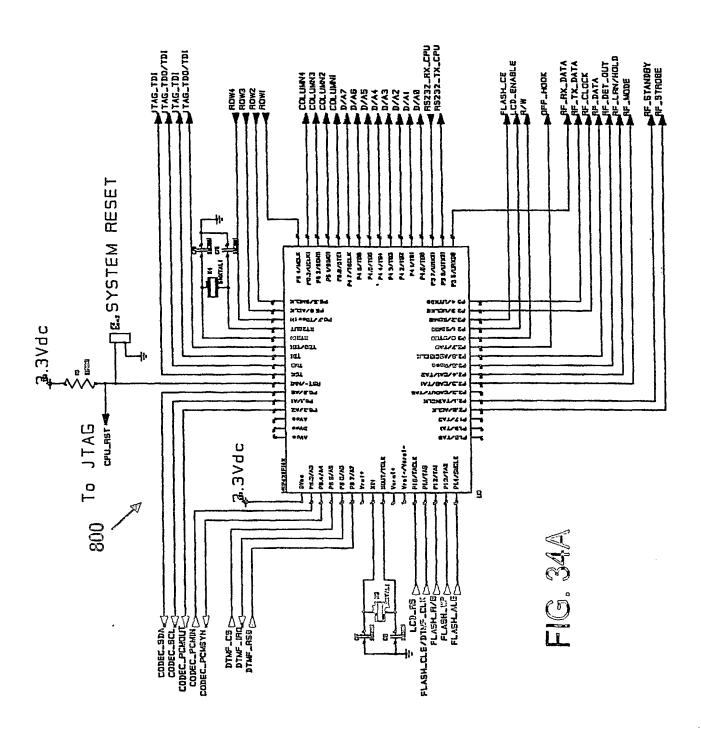
SUBSTITUTE SHEET (RULE 26)

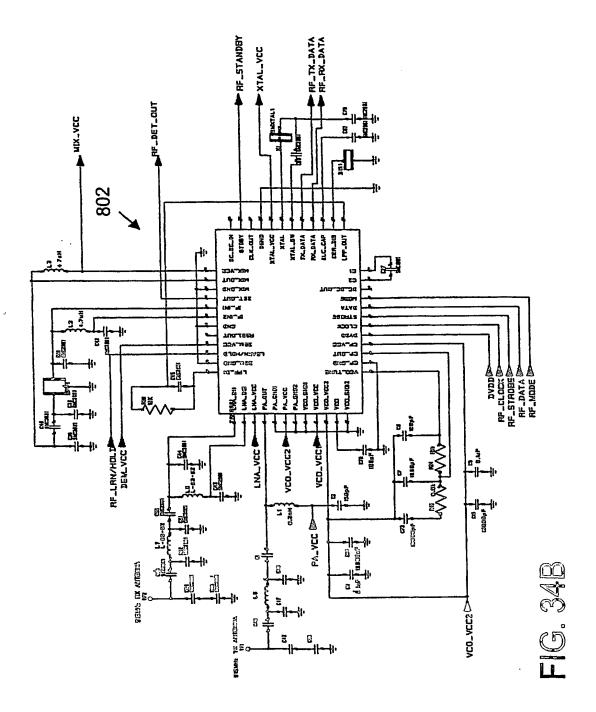


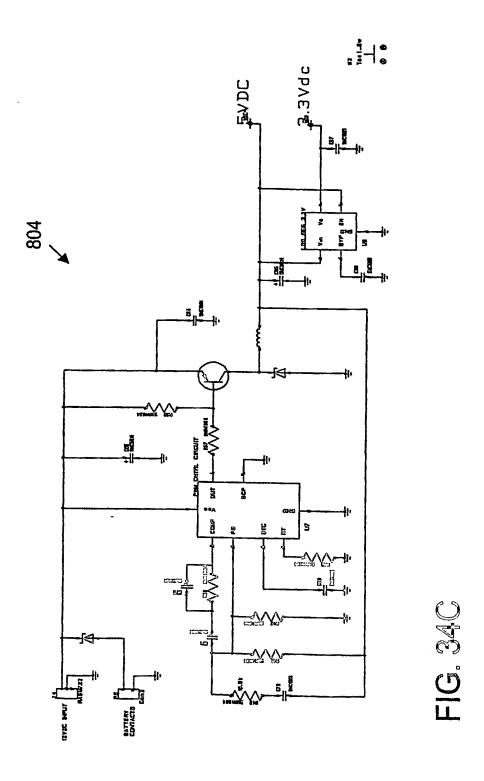
SUBSTITUTE SHEET (RULE 26)

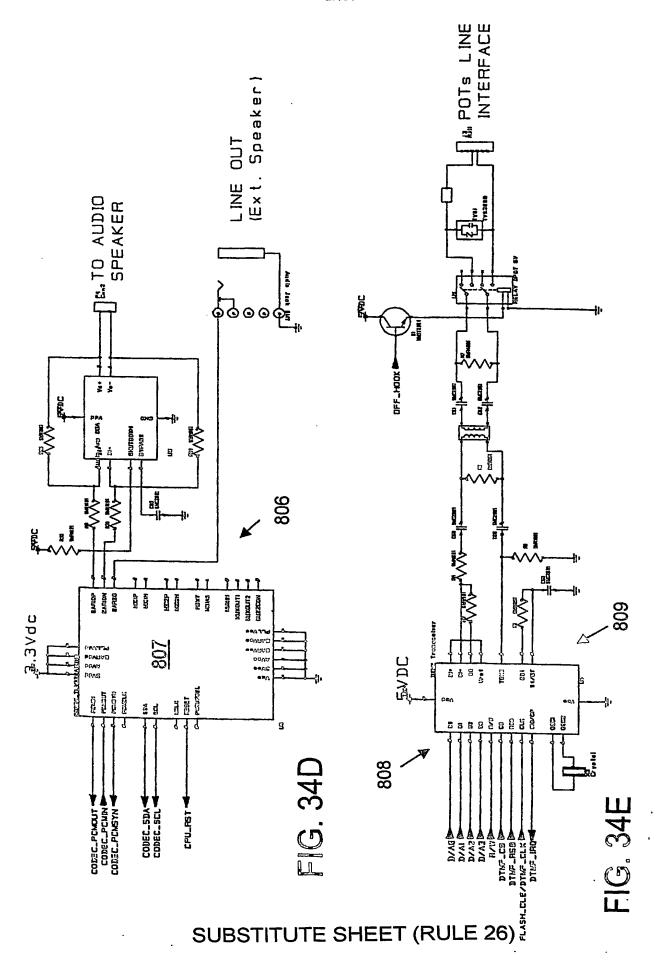


SUBSTITUTE SHEET (RULE 26)









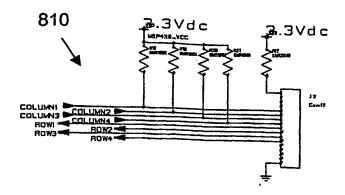
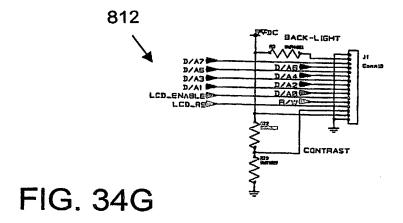


FIG. 34F



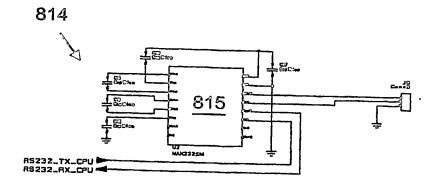
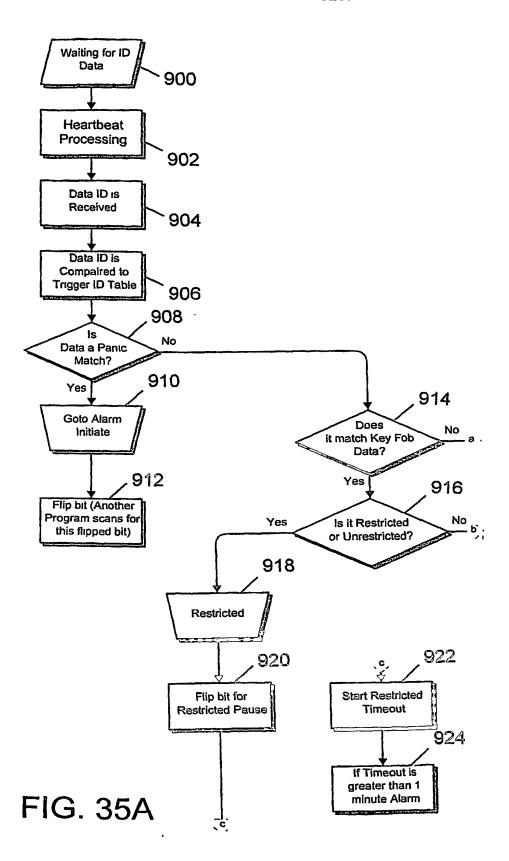


FIG. 34H



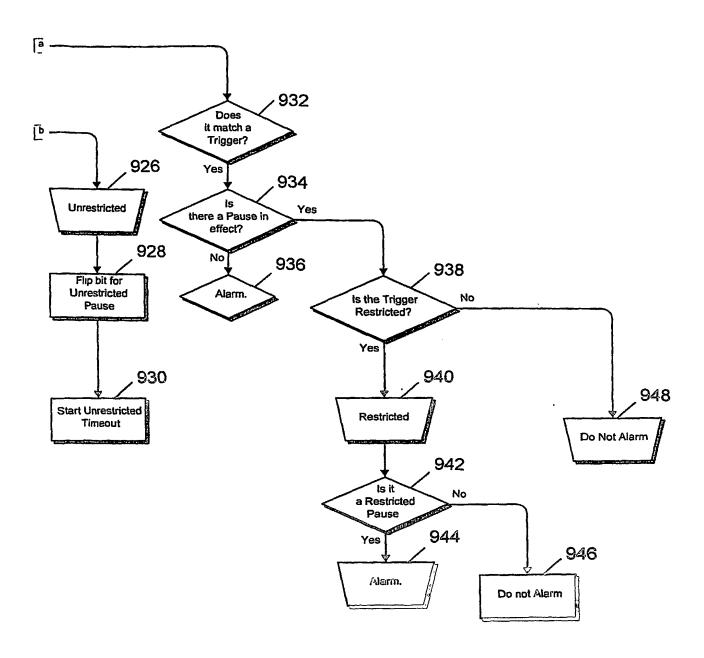
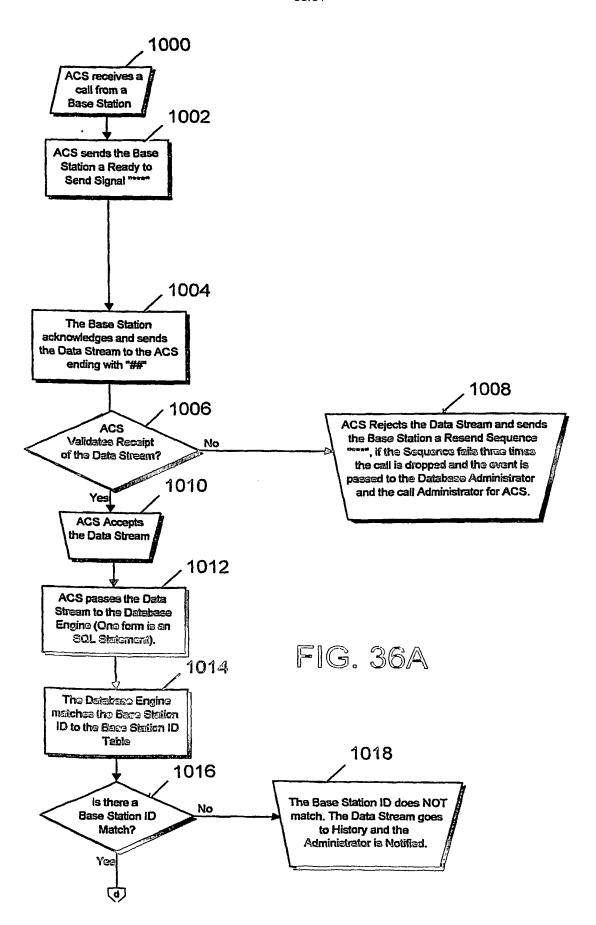
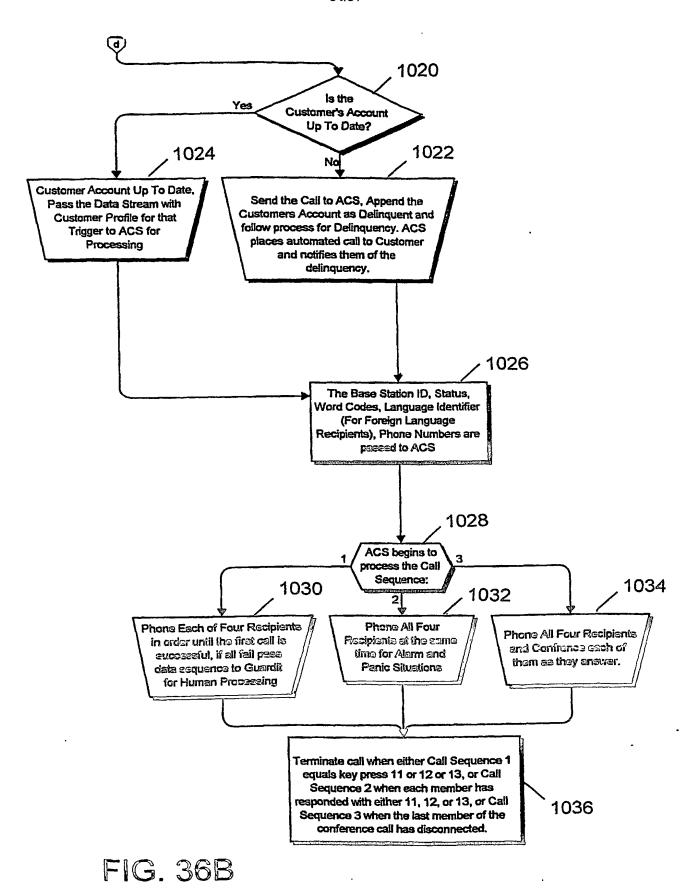


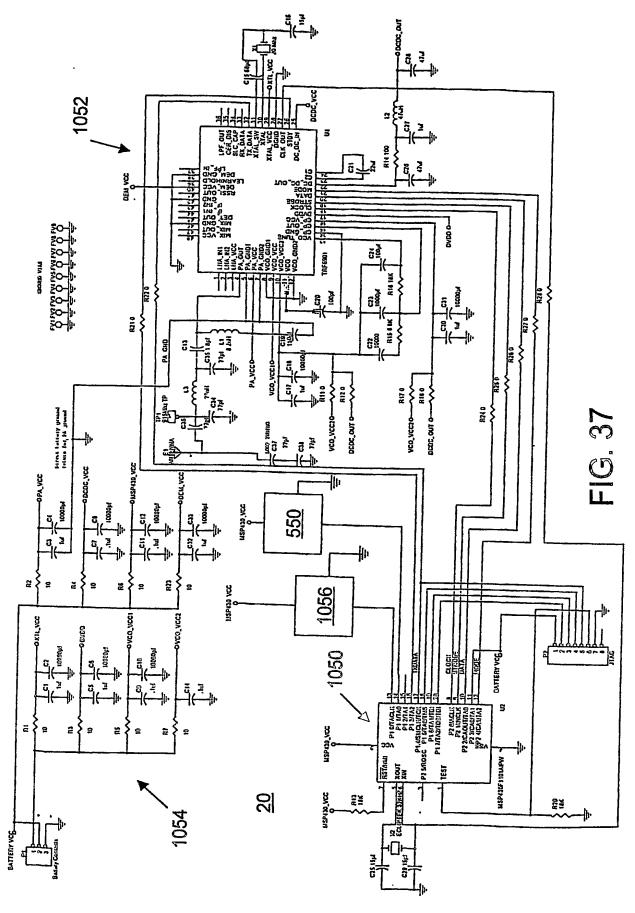
FIG. 35B



SUBSTITUTE SHEET (RULE 26)



SUBSTITUTE SHEET (RULE 26)



SUBSTITUTE SHEET (RULE 26)

